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Amendments to the Specification

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NOV 20 2007

Please amend the paragraph bridging pages 3 and 4, in the following manner:

This patent specification describes a novel semiconductor device used in a mobile phone that processes digital signals. In one example, a novel semiconductor device includes a first converter, a second converter, a first digital ~~processor~~ processing circuit, and a second digital ~~processor~~ processing circuit. The first converter converts a digital audio signal sampled with a predetermined audio sampling frequency for a digital audio signal into a digital audio signal sampled with a predetermined voice sampling frequency for a voice signal. The second converter converts a digital voice signal sampled with the predetermined voice sampling frequency into a digital voice signal sampled with the predetermined audio sampling frequency. The first digital ~~processor~~ processing circuit performs a predetermined digital computation on the digital audio signal sampled with the predetermined voice sampling frequency and a digital voice signal. The second digital ~~processor~~ processing circuit configured to perform the predetermined digital computation on the digital voice signal sampled with the predetermined audio sampling frequency and the digital audio signal sampled with the predetermined audio sampling frequency.

Please amend the paragraphs at page 4, line 20 through page 5, line 1, in the following manner:

The first digital ~~processor~~ processing circuit may be configured to perform a volume setting on a digital signal processed with the predetermined digital computation.

The first digital ~~processor~~ processing circuit may be configured to perform a signal band restriction on a digital signal processed with the predetermined digital computation.

Please amend the paragraphs at page 5, lines 4-9, in the following manner:

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The second digital ~~processor~~ processing circuit may be configured to perform a volume setting on a digital signal processed with the predetermined digital computation.

The second digital ~~processor~~ processing circuit may be configured to perform a signal band restriction on a digital signal processed with the predetermined digital computation.

Please amend the paragraph bridging pages 5 and 6, in the following manner:

The voice DA converter converts a second digital voice signal sampled with a predetermined voice sampling frequency for a voice signal into an analog voice signal. The voice output device generates a voice sound in accordance with the analog voice signal output from the voice digital-to-analog converter. The audio DA converter converts a digital audio signal sampled with a predetermined audio sampling frequency for an audio signal into an analog audio signal. The audio output device generates an audio sound in accordance with the analog audio signal output from the audio DA converter. The semiconductor device processes and outputs the first digital voice signal, the second digital voice signal sampled with the predetermined voice sampling frequency, and the digital audio signal sampled with the predetermined audio sampling frequency. The semiconductor device includes a first converter, a second converter, a first digital ~~processor~~ processing circuit, and a second digital ~~processor~~ processing circuit.

Please amend the abstract on page 41, in the following manner:

A method and apparatus of mobile phone using a semiconductor device includes a first converter, a second converter, a first digital ~~processor~~ processing circuit, and a second digital ~~processor~~ processing circuit. The first converter converts a first digital audio signal sampled with a predetermined audio sampling frequency into a second digital audio signal sampled with a predetermined voice sampling frequency. The second converter converts a first digital voice signal sampled with the

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predetermined voice sampling frequency into a second digital voice signal sampled with the predetermined audio sampling frequency. The first digital ~~processor~~ processing circuit performs a predetermined digital computation on the second digital audio signal sampled with the predetermined voice sampling frequency and a third digital voice signal. The second digital ~~processor~~ processing circuit performs the predetermined digital computation on the second digital voice signal sampled with the predetermined audio sampling frequency and the first digital audio signal sampled with the predetermined audio sampling frequency.